

<b>Notice of References Cited</b>		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		10/676,890	JEYASINGH ET AL.	
		Examiner	Art Unit	Page 1 of 1
		Gopal C. Ray	2111	

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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*	B	US-5,452,462	09-1995	Matsuura et al.	718/1
*	C	US-6,870,899	03-2005	Lu et al.	379/1.04
*	D	US-2005/0210467	09-2005	Zimmer et al.	718/001
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Test floor verification of multiprocessor hardware" by Saha et al. (abstract only) Publication Date: 27-29 March 1996
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.